IN THE CLAIMS:

- 1. (Once Amended) A package for containing semiconductor element comprising:
- a [package] <u>housing</u> having a recess portion for containing a semiconductor element; and

a pair of positioning holes and a pair of attaching holes respectively provided at [a pair of] opposed side portions of said [recess portion at a surface of said package] housing,

wherein a line [connecting] <u>between</u> said pair of positioning holes and a line [connecting] <u>between</u> said pair of attaching holes [are intersected] <u>intersect</u> with each other substantially at a center of said package <u>and further wherein the line between the positioning holes is skewed with respect to each of the side walls of the housing and the line between the <u>attaching holes is skewed with respect to each of the side walls.</u></u>

- 3. (Once Amended) A semiconductor device comprising:
- a semiconductor element;
- a [package] <u>housing</u> having a recess portion for containing said semiconductor element; and

a pair of positioning holes and a pair of attaching holes respectively provided at [a pair of] opposed side portions of said [recess portion at a surface of said package] housing;

wherein a line [connecting] <u>between</u> the pair of positioning holes and a line [connecting] <u>between</u> said pair of attaching holes [are intersected] <u>intersect</u> with each other substantially at a center of said package <u>and further wherein the line between the positioning holes is skewed with respect to each of the side walls of the housing and the line between the attaching holes is skewed with respect to each of the side walls.</u>

- 5. (Once Amended) A semiconductor device comprising:
- a semiconductor element;
- a [package] <u>housing</u> having a recess portion for containing said semiconductor element;
- a pair of attaching holes provided at [a pair of] opposed side portions of said [recess portion] housing at a surface of said package; and
- a transparent member for sealing said semiconductor element in said recess portion; wherein said surface of said [package] housing is made to be higher than a top surface of said transparent member.